# finite the second secon

Transparent and secure chip solutions

**TROPIC01 - SECURE ELEMENT** 

# A fully transparent and auditable chip as a basis for better hardware security.



Designed for security





#### THE PAIN POINT WE TACKLE

# SatoshiLabs and Trezor hardware wallet

- Open source
- Self-sovereignty
- Do not trust, verify
- Security

## The current chip market

- Restrictive NDAs
- Unable to share discoveries
- Closed solutions
- Have to trust, can not verify
- Flaws remain hidden
- User community is unable to repair or improve



PAIN POINT WE TACKLE

The chip market as it's set up currently cannot deliver on the high standards of security and transparency companies demand when it comes to protecting hardware.



THE ROAD TO SECURITY

# a TRully OPen IC



#### APPROACH TO SECURITY

## Kerckhoff's principles

- "One ought to design systems under the assumption that the enemy will immediately gain full familiarity with them"
- A guiding mantra of security experts, • but not practically implemented yet
- **Designed for security**

## **Security in obscurity**

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- •
- be retired

Relying on design or implementation secrecy as the main method of providing security to a system or component

Keeps weak designs in production

Reduces the need to innovate

Old designs used long after they should



PRODUCT

## Transparency as a driver of innovation

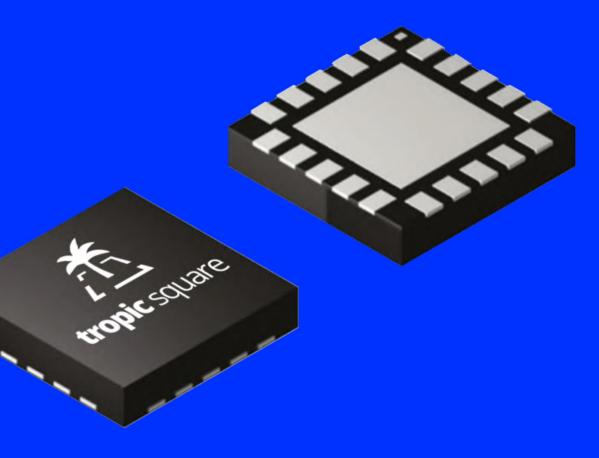


PRODUCT

# TROPICO1

The first auditable secure chip





#### AUDITABLE PART

Data path to show how user's data are handled in ciphertext / plaintext

Cryptography implementation and key handling

Noise protocol secure channel

ECDSA, EdDSA, ECDH

Authentication and attestation

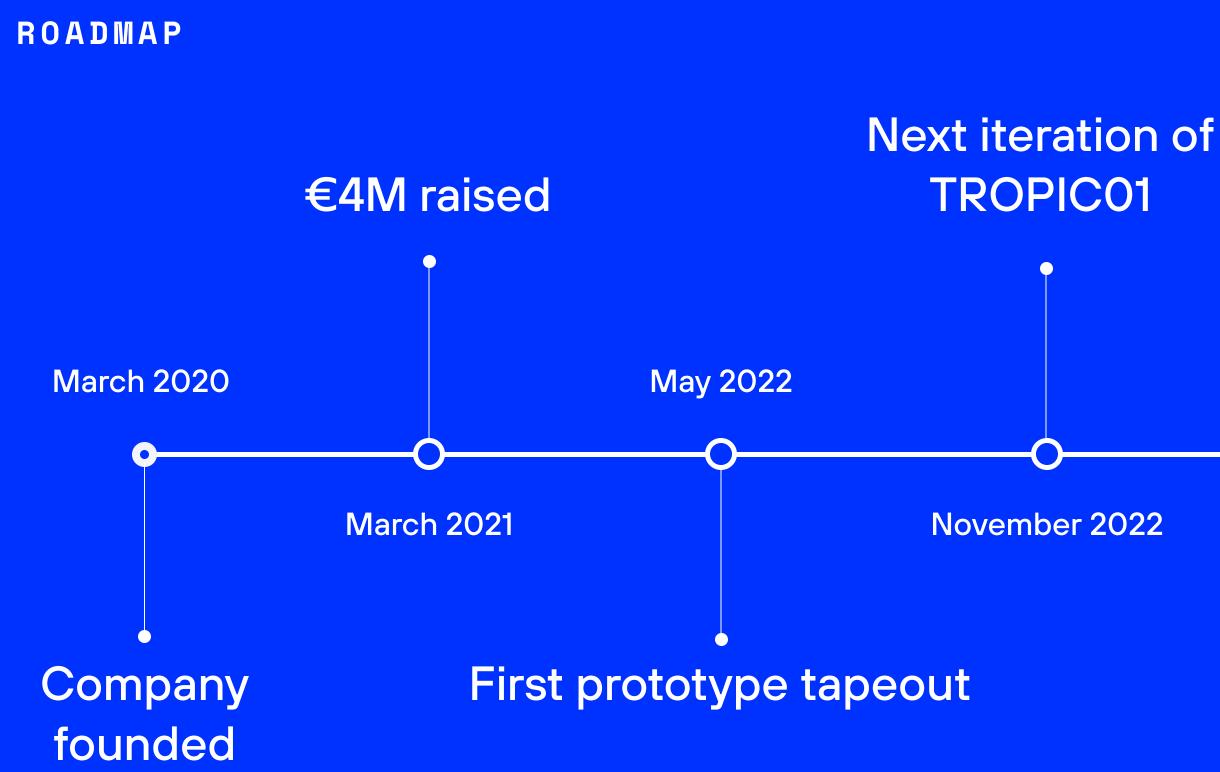
**RISC-V** core and firmware



#### CLOSED PART (FOR NOW)

Srd party IP cores OTP, PUF, eFuse, Flash Attack detectors Laser, EM, Glitch detectors Active shield Chip infrastructure power supplies







November 2023



## **TROPIC01** samples tapeout



#### **SUPPORT**

## EU HORIZON-CL3-2021 ORSHIN

- Reduce security threats to open source hardware for connected devices
- Address Tropic's long term vision of transparency & security
- 7 partners Academia, IDM, SMEs

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## **Czech Universities**

Supported by National Cyber and Information Security Agency

The transparency and security of Tropic's chip makes it a perfect target for demonstration



#### **BEYOND THE TROPIC01**

### **TROPICO2** and **O1** derivatives

• Secure SoC with TROPIC01

### **Embedded security eco-system**

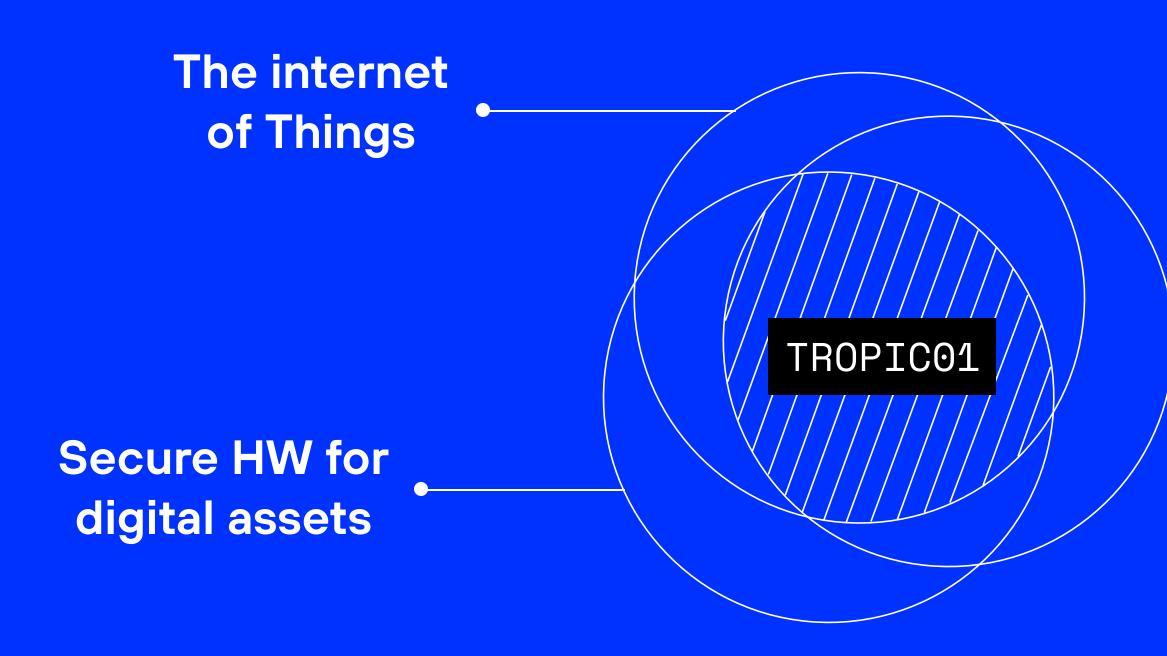
- System solutions on top of TROPICxx chips
- Development services for 3rd party

### Fabless chip design studio

 Success with TROPIC01/02 will be transformed into sustainable fabless model of transparent secure solutions

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INTERSECTION OF BOOMING MARKETS







#### CO-FOUNDERS & TEAM

### **Evzen Englberth, CEO**

- 15yrs experience in EMS
- Managed the development/production of Trezor

## Jan Pleskac, CTO

- 20 years in ASIC/FPGA design services
- Worked for Intel, Altera, S3 Group and their customers

## Stick & Slush

Co-founders of SathoshiLabs, advisors to Tropic

## **Technical team**

- 12 internal developers
- Network of external experts and partners in chip design and security

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#### UP SUMMTNG

- Something genuinely new in the market a fully  $\bigcirc$ transparent and auditable security chip as a basis for better hardware security
- $\overline{\mathbf{a}}$
- Our transparent approach aligns with best cryptography practice and will result in better security, delivered faster
- \$
- A viable route to commercialising and scaling the technology





